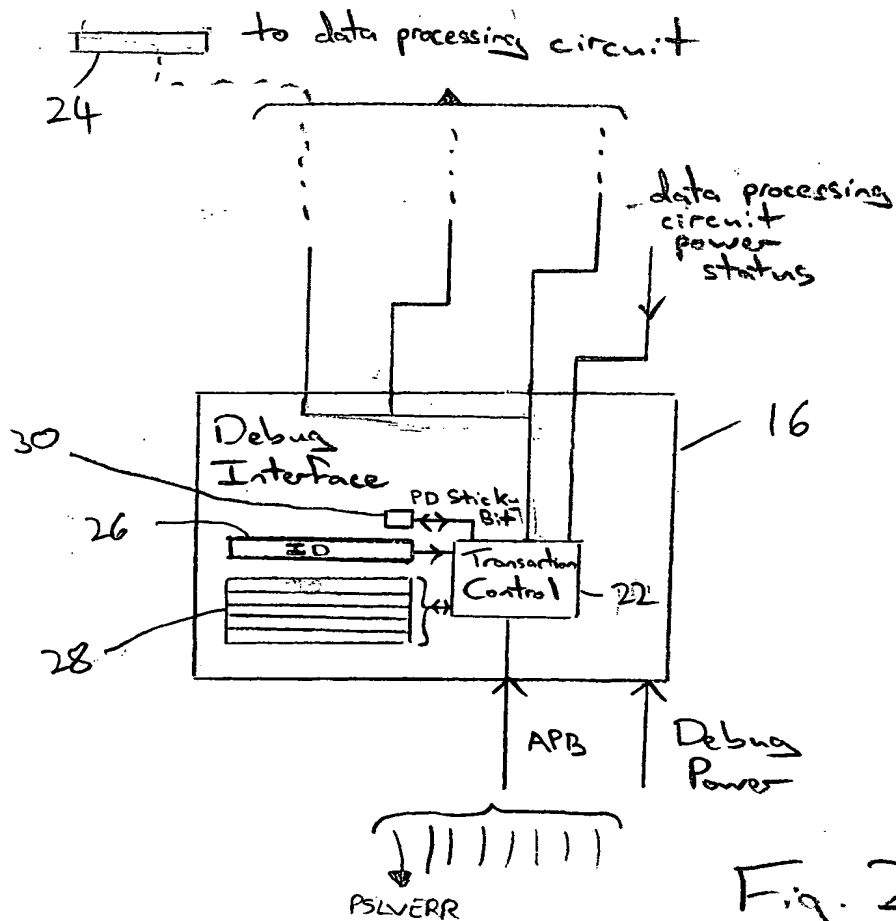
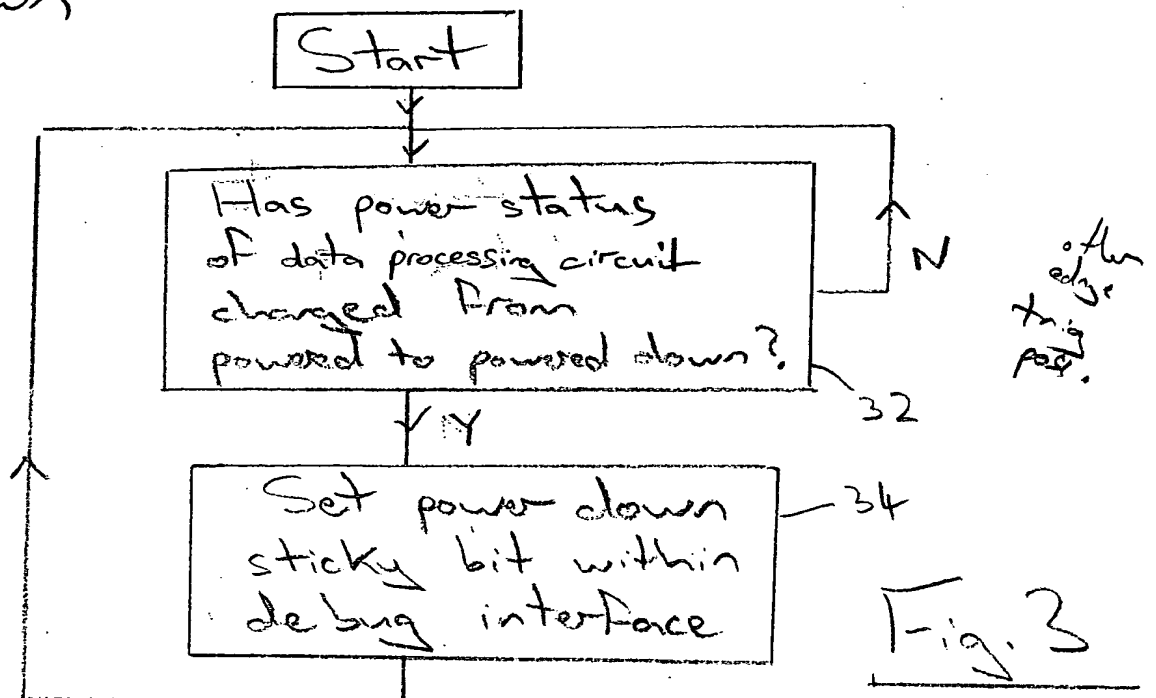


2

Fig. 1



Power Down
Sticky
Bit
Setting



Debug Bus Error Generation

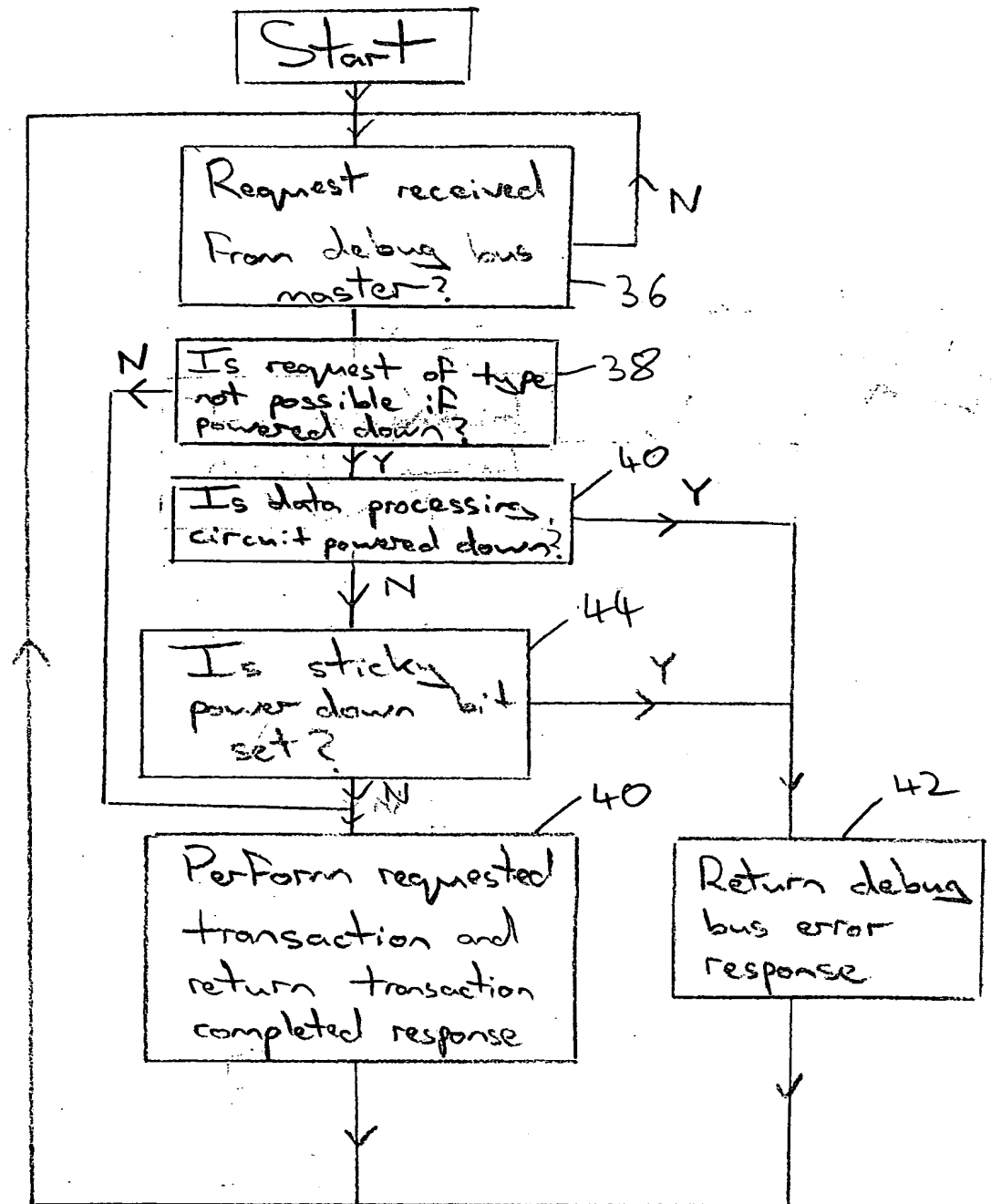


Fig. 4